

Qam Modulator Fpga Ip Core Iprrium

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Qam Modulator Fpga Ip Core

QAM Modulator IP Core W_DAC Width of output DAC symbols (odati/odatq) Increasing the width of odati/odatq, increases the quality of waveform but also increases FPGA required resource CONFIG Set of mapping tables and QAM/PSK constellations.

QAM Modulator FPGA IP Core - IPrium

QAM Modulator FPGA IP-Core Modulation von DVB-C nach ETSI-EN 300429 V1.2.1 •
Modulationsordnungen: QAM16, QAM32, QAM64, QAM128, QAM256 • Symbolrate 1000-7000

Online Library Qam Modulator Fpga Ip Core Iprrium

kSym/s • Implementierung mit einem 27MHz Quarz • Integrierter Mischer auf ZF und Interpolations-Filter • ZF-Ausgabe einstellbar zwischen 3.5MHz und 70MHz

QAM Modulator FPGA IP-Core - maintech

Modulators / Demodulators. The IP Core implements the QAM modulation with filtering and signal interpolation. Mapper Pulse Shaping Filter Resampler Quadrature Modulator NCO DDS Version : 4.0 Build date : 2014.09 Ordering code : ip-qam-modulator Supported technologies : FPGA (Xilinx, Intel/Altera, Lattice, Microsemi/Actel), ASIC (Digital ASIC)

QAM Modulator IP Core - FPGA IP Cores, wireless modems, FEC

mehrfachen QAM-Modulation mit DUC als FPGA-Lösung. Schnelle Evaluationsplattformen: Xilinx Virtex-6 LX130T Evaluation Kit von AVNET Analog Devices AD9739-CMTS-EBZ Evaluation Kit (Abb.) IP Core maintechs IP-Cores stehen in verschiedenen Versionen, vom fertigen Binär-Image bis zum kompletten Source-Code, zur Verfügung. Der 16fach QAM & DUC IP ...

16fach QAM & DUC FPGA IP-Core - maintech

These IP cores can meet High speed data rate applications on FPGA's UTS offers IPs under flexible licensing models, to meet requirements of different category of users. The low cost reference designs are distinguished from IPs with less (or no) document (end user/project) execution for licensing.

UTS - QAM Modulator and Demodulator IP core

QAM Modulator FPGA IP-Core DVB-C Modulation According to ETSI-EN 300429 V1.2.1 • Constellations: QAM16, QAM32, QAM64, QAM128, QAM256 • Symbol rate: 1000-7000 ksym/s • Implementation using a single 27MHz crystal • Integrated IF upconverter and interpolation filter • IF output adjustable between 3.5MHz and 70MHz

QAM Modulator FPGA IP-Core - jordaanelectronics.com

Modulators / Demodulators. The IP Core implements the QAM signal demodulation using Costas and Gardner schemes. Quadrature Demodulation Decimator/ Resampler Matched Filter Decision Slicer DDS Recovery External AGC Version : 3.0 Build date : 2014.09

QAM Demodulator IP Core - FPGA IP Cores, wireless modems, FEC

QAM Demodulator IP Core QAM Demodulator IP Core Release Information Name QAM Demodulator IP Core Version 3.0 Build date 2014.09 Ordering code ip-qam-demodulator Specification revision r1465 Features The IP core implements the full-featured QAM demodulation algorithm and is fully compatible with those standards:

QAM Demodulator FPGA IP Core - IPrium

DVB-C J.83 Annex A/C modulator : The DVB-C J.83A/C cable modulator modulates an MPEG-TS DVB-SPI input into a QAM-16/32/65/128/256 output in base band I/Q.

QAM modulator - DVB-C J.83 Annex A/C modulator - IP core ...

J.83 Annex B cable modulator : The J.83B cable modulator modulates an MPEG-TS DVB-SPI input into a QAM-16/256 output in baseband I/Q.

QAM modulator - J.83 Annex B - IP core for FPGA

16ch QAM & DUC FPGA IP-Core DVB-C Modulation According to ETSI-EN 300429 V1.2.1 with Digital Upconversion • QAM Modulation according to ETSI EN 300 429 V1.2.1 • 16 DVB-C channels to 7000 kSym/s max • Signal quality: MER > 44dB • Input: MPEG2 transport stream • Output on AD9739 (or similar DAC)

16ch QAM & DUC FPGA IP-Core - Jordaan Electronics

Full DVB-C J.83 Annex A/C modulator - RF output with AD9789 DAC : The DVB-C J.83A/C cable modulator modulates an MPEG-TS DVB-SPI input into a QAM-16/32/65/128/256 output in base band for AD9789 RF DAC.

QAM modulator - MVD Cores - IP core solutions for Digital ...

QAM modulator - J.83 Annex B - RF output with AD9789 DAC J.83 Annex B cable modulator - RF output with AD9789 DAC ... The MVD Cable modulator J.83B core is a drop-in module for FPGA that includes the following functions: Input data framer from DVB-SPI source (MPEG-TS flow) ...

QAM modulator - J.83 Annex B - RF output with AD9789 DAC ...

Full digital demodulator supports 8-VSB and 64/256-QAM. Fully separable core for optional OOB forward data channel. Prototyped in FPGA and tested in the lab and in the field by independent ... Fully separable core for optional OOB forward data channel.

Xilinx Modulation/Demodulation IP Core - Design And Reuse

Complete datasheets for Xilinx qam products ... The MVD DVB-T/H modulator is a drop-in module for FPGA that includes the following functions : • Input data framer from Microprocessor, DVB-SPI source (MPEG-TS flow) • DVB-T modulator Energy ... 6. EdgeQAM IP Core Set (1 x RF Port, 160-Ch, ADI) Developed in collaboration with Xilinx®, Inc., the RADX™ EdgeQAM IP Core Set is a modular ...

Xilinx qam IP core / Semiconductor IP / Silicon IP

Versatile Digital QAM Modulator Combined with enhanced DSP blocks, the logic structure of Altera's Stratix® series and Arria® series FPGAs offer a flexible way to implement the key elements of the Quadrature Amplitude Modulation (QAM) signal-processing path in any region.

Versatile Digital QAM Modulator - Intel

FPGA intellectual property (IP) provider IPrium LLC today announced that it has added 128-APSK and 256-APSK (DVB-S2X) constellation support to the QAM Modulator IP Core. The QAM Modulator IP Core has been silicon-proven using multiple development boards and is compliant with DVB Document A83-2 Part II.

IPrium adds 128-APSK and 256-APSK support to QAM Modulator ...

List of IP Cores (total 30 IP Cores) IP Core development company. Email: moc.muirpi@ofni . IPrium ... QAM Modulator Build date: 2014.09 from 900 € from 9500 € ...

List of IP Cores (total 30 IP Cores) - IPrium

D&R provides a directory of Quadrature Amplitude Modulation IP Core. The High Data Rate Demodulator IP core can demodulate BPSK, QPSK, offset-QPSK (OQPSK), 8PSK and 16QAM modulation schemes, all to a high performance level and at high symbol rates.

Quadrature Amplitude Modulation IP Core - Design And Reuse

maintech's IP-Core unites all advantages of the DVB-S2 modulation: The IP-Core provides a powerful LDPC error corection, all constellation types as QPSK, 8PSK, 16APSK, 32APSK and the roll off factors 0.2, 0.25 and 0.35.

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